7th International Conference on Recent Trends in Image Processing and Pattern Recognition

December 19-20, 2024

Indian Institute of Information Technology Bhopal, India
in collaboration with Maulana Azad National Institute of Technology Bhopal, India
and 2AI: Applied Artificial Intelligence Research Lab - University of South Dakota, USA

HYBRID EVENT

ST7: ADVANCEMENTS IN MACHINE LEARNING AND PATTERN RECOGNITION FOR ELECTRICAL & ELECTRONICS ENGINEERING

About Special Track:

Machine Learning and Pattern Recognition are revolutionizing Electrical and Electronic (EEE) systems by enabling predictive maintenance, optimizing equipment performance, and reducing downtime through failure prediction. Their integration in EEE fosters systems such as smart grids, autonomous vehicles, drones, and renewable energy sources by boosting decision-making, automation, and system reliability. Researchers are invited to submit their original research findings to enhance the value and knowledge base within this domain.

Topics of Interest:

* Smart Grid
* Autonomous Vehicles and Drones
* Power System Optimization & Automation
* Energy Management System
* Signal Processing for Electrical Systems
* AI & ML applications for EEE Systems

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Main Track (RTIP2R-2024)
ST1: Advancements in AI for Image Processing
ST2: AI in Healthcare
ST3: Emotional well-being Digital Twin
ST4: Cross-Modal Information Retrieval using Image and Textual Contents
ST5: Quantum-Enhanced Imaging and ML for Environmental Analysis
ST6: Cyber-Physical Systems Automation for Industry 4.0
ST7: Advancements in Machine Learning and Pattern Recognition for Electrical & Electronics Engineering
ST8: Machine Learning Techniques for Image, Speech and Natural Language Processing

Paper submission: Papers are expected to be within the **4–6 page**. The authors are advised to follow **Guidelines** to prepare their manuscript.

Templates: MS-Word Template or **LaTeX Template**
Note: We recommend using the **LaTeX** template.

Please select ST7: Advancements in Machine Learning and Pattern Recognition for Electrical & Electronics Engineering from tracks before you begin your submission as shown.

SUBMISSION URL: https://cmt3.research.microsoft.com/RTIP2R2024

Deadline: 30/05/2024

TRACK CHAIRS

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PUBLICATIONS

Conference proceedings will be published in **Procedia Computer Science, Elsevier**.
Selected and/or best papers may be extended for journal issues (SCI, Scopus, and DBLP indexed).

Scopus Indexed

For any query, please contact at **ishanjss28@gmail.com**

http://www.rtip2r-conference.org/2024/